Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

Claims 1-12.   (Cancelled)

Claim 13.   (Original)   An integrated circuit comprising:

an arbiter configured to track a number of available entries in a posted request

FIFO;

a plurality of clients coupled to the arbiter; and

a HyperTransport bus coupled to the arbiter;

wherein the arbiter receives peer-to-peer requests from the plurality of clients and

provides posted requests to the posted request FIFO, and

when the number of available entries in the posted request FIFO is equal to a first

number, then preventing the plurality of clients from sending peer-to-peer requests.

Claim 14.   (Original)   The integrated circuit of claim 13 wherein the

plurality of clients includes a graphics processor.

Claim 15.   (Original)   The integrated circuit of claim 14 wherein the

plurality of clients further includes a PCI-to-PCI bridge.

Claim 16.   (Original)   The integrated circuit of claim 13 wherein the

number of pending peer-to-peer requests is incremented by one for each granted posted request.

Claim 17.   (Original)   The integrated circuit of claim 16 wherein the

number of pending peer-to-peer requests is decremented by one for each peer-to-peer request

provided by a receive FIFO.

Claim 18. (Original) The integrated circuit of claim 13 further comprising when the number of pending peer-to-peer requests is less than the first number, allowing the number of clients to issue a peer-to-peer request.

Claim 19. (Original) The integrated circuit of claim 18 wherein the first number is one.